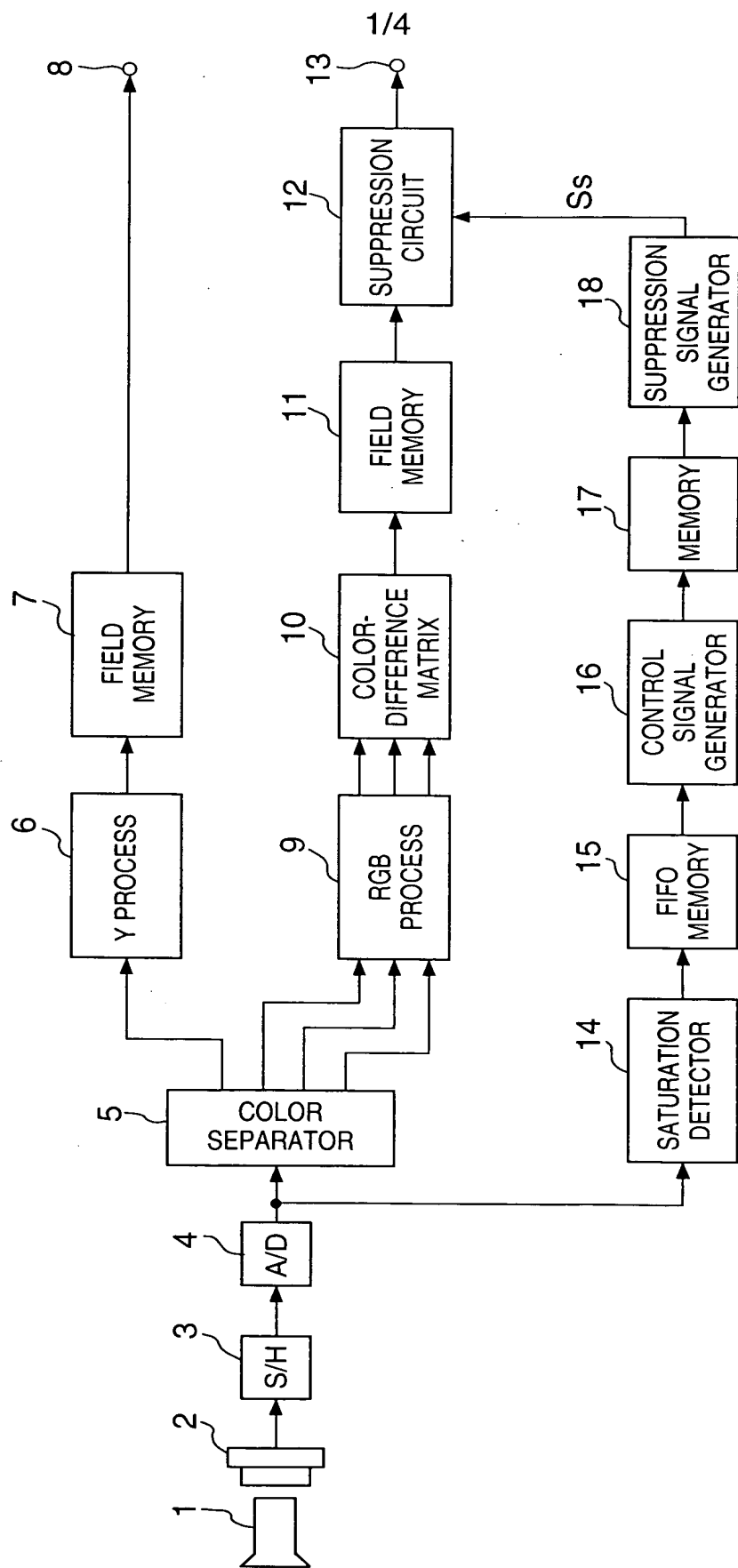
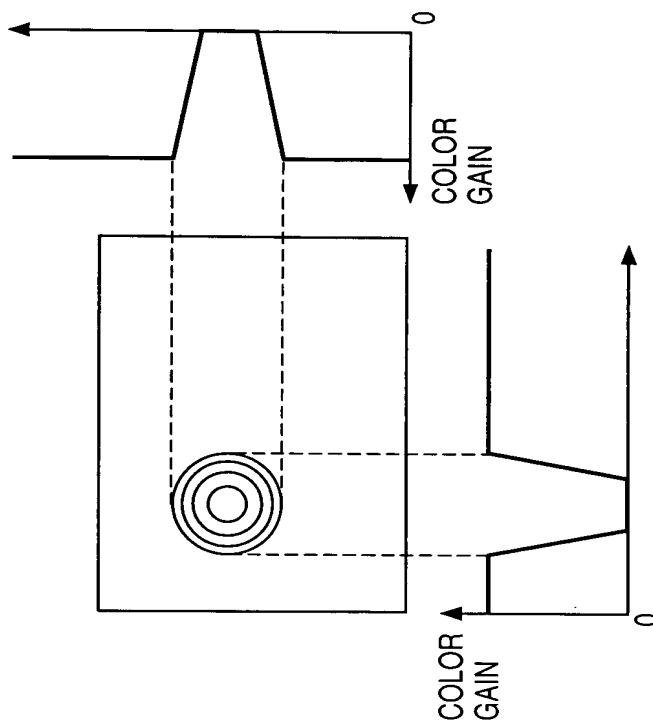
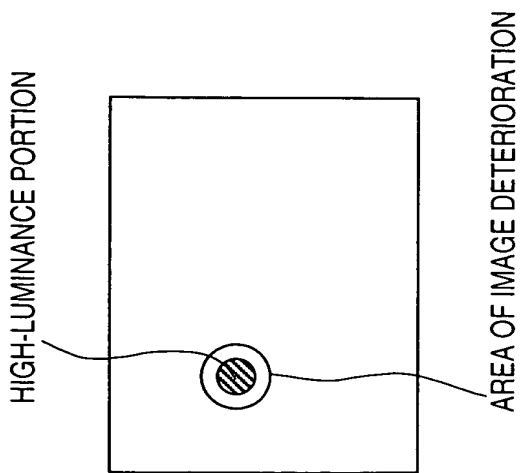


FIG. 1



**FIG. 2B**

# SUPPRESSION CHARACTERISTIC OF COLOR SIGNAL IN AREA OF IMAGE DETERIORATION



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The diagram illustrates the internal architecture of a semiconductor device, divided into several functional blocks:

- Block 14:** Contains a **THRESHOLD VALUE** (101) and a **COMPARATOR** (102). It receives **SADO** as input and outputs **Sw** to the **FIFO** (15).
- Block 15:** A **FIFO** (First In First Out) buffer that receives **Sw** and outputs **SRD** and **SFE** to the **READ-REGISTER** (104) and **PATTERN CONTROL** (103) respectively.
- Block 16:** A central processing block containing:
  - READ-REGISTER** (104): Receives **SRD** and **SFE**. It outputs **AHS** to the **ADDRESS GENERATOR** (105) and **AVS** to the **PATTERN GENERATOR** (108).
  - ADDRESS GENERATOR** (105): Receives **AHS** and outputs **AV** to the **MEMORY** (17) and **DW** to the **READ-REGISTER** (104).
  - PATTERN GENERATOR** (108): Receives **AVS** and outputs **DR** to the **MEMORY** (17).
  - PATTERN CONTROL** (103): Receives **SFE** and outputs **DR** to the **MEMORY** (17).
  - READ-OUT ADDRESS GENERATOR** (109): Receives **HDO** and **VDO** as inputs. It outputs **DR** to the **MEMORY** (17) and **Ss** to the **LEVEL CONVERTER** (110).
  - MEMORY** (17): Receives **AV** and **DW** from the **ADDRESS GENERATOR** (105) and **DR** from the **PATTERN GENERATOR** (108) and **PATTERN CONTROL** (103). It outputs **AH** to the **READ-REGISTER** (104).
  - LEVEL CONVERTER** (110): Receives **Ss** from the **READ-OUT ADDRESS GENERATOR** (109) and outputs **Ss** to the external output.

External inputs are **SADO**, **PHO**, **PVO**, **HDO**, and **VDO**. The final output is **Ss**.

FIG. 4

